

Refine Search

Search Results -

| Terms | Documents |
|----------------------|-----------|
| L2 same programmable | 15 |

Database:

US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search:

Refine Search

Recall Text

Clear

Interrupt

Search History

DATE: Monday, June 20, 2005 [Printable Copy](#) [Create Case](#)

Set Name Query

side by side

DB=PGPB,USPT,USOC; PLUR=YES; OP=OR

Hit Count Set Name
result set

| | | | |
|-----------|---|-----|-----------|
| <u>L3</u> | L2 same programmable | 15 | <u>L3</u> |
| <u>L2</u> | bridge same "state machine" same processor | 142 | <u>L2</u> |
| <u>L1</u> | bridge same "state machine" same "programmable processor" | 1 | <u>L1</u> |

END OF SEARCH HISTORY

Refine Search

Search Results -

| Terms | Documents |
|-------|-----------|
| L3 | 0 |

Database:

US Pre-Grant Publication Full-Text Database
 US Patents Full-Text Database
 US OCR Full-Text Database
 EPO Abstracts Database
 JPO Abstracts Database
 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

Search:

| | | |
|--|--|--|
| L4 | <input type="button" value="Refine Search"/> | |
| <input type="button" value="Recall Text"/> | <input type="button" value="Clear"/> | <input type="button" value="Interrupt"/> |

Search History

DATE: Monday, June 20, 2005 [Printable Copy](#) [Create Case](#)

Set Name Query

side by side

DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

L4 L3

Hit Count Set Name

result set

0 L4

DB=PGPB,USPT,USOC; PLUR=YES; OP=OR

L3 L2 same programmable15 L3L2 bridge same "state machine" same processor142 L2L1 bridge same "state machine" same "programmable processor"1 L1

END OF SEARCH HISTORY

Hit List

[Clear](#)[Generate Collection](#)[Print](#)[Fwd Refs](#)[Bkwd Refs](#)[Generate OACS](#)

Search Results - Record(s) 1 through 10 of 15 returned.

1. Document ID: US 20050060479 A1

L3: Entry 1 of 15

File: PGPB

Mar 17, 2005

PGPUB-DOCUMENT-NUMBER: 20050060479

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20050060479 A1

TITLE: High speed and flexible control for bridge controllers

PUBLICATION-DATE: March 17, 2005

INVENTOR-INFORMATION:

| NAME | CITY | STATE | COUNTRY | RULE-47 |
|----------------------|------------|-------|---------|---------|
| Deng, Brian Tse | Richardson | TX | US | |
| Nie, Dinghui Richard | Plano | TX | US | |
| Erickson, Joseph M. | Frisco | TX | US | |

US-CL-CURRENT: 710/306[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#) [Sequences](#) [Attachments](#) [Claims](#) [KMC](#) [Draw Desc](#) [Image](#)

2. Document ID: US 20040030861 A1

L3: Entry 2 of 15

File: PGPB

Feb 12, 2004

PGPUB-DOCUMENT-NUMBER: 20040030861

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20040030861 A1

TITLE: Customizable computer system

PUBLICATION-DATE: February 12, 2004

INVENTOR-INFORMATION:

| NAME | CITY | STATE | COUNTRY | RULE-47 |
|-----------------|---------|-------|---------|---------|
| Plackle, Bart | Diest | | BE | |
| Herremans, Kurt | Hasselt | | BE | |

US-CL-CURRENT: 712/32[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#) [Sequences](#) [Attachments](#) [Claims](#) [KMC](#) [Draw Desc](#) [Image](#)

3. Document ID: US 20030191920 A1

L3: Entry 3 of 15

File: PGPB

Oct 9, 2003

PGPUB-DOCUMENT-NUMBER: 20030191920
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20030191920 A1

TITLE: Method and device to use memory access request tags

PUBLICATION-DATE: October 9, 2003

INVENTOR-INFORMATION:

| | | | | |
|------------------|-----------|-------|---------|---------|
| NAME | CITY | STATE | COUNTRY | RULE-47 |
| LaBerge, Paul A. | Shoreview | MN | US | |

US-CL-CURRENT: 711/173; 711/170

[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#) [Sequences](#) [Attachments](#) [Claims](#) [KIMC](#) [Drawn Desc](#) [Image](#)

4. Document ID: US 20020178317 A1

L3: Entry 4 of 15

File: PGPB

Nov 28, 2002

PGPUB-DOCUMENT-NUMBER: 20020178317
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20020178317 A1

TITLE: System and method for defining private functions of a multi-function peripheral device

PUBLICATION-DATE: November 28, 2002

INVENTOR-INFORMATION:

| | | | | |
|----------------------|----------|-------|---------|---------|
| NAME | CITY | STATE | COUNTRY | RULE-47 |
| Schmissleur, Mark A. | Phoenix | AZ | US | |
| Luse, Paul E. | Chandler | AZ | US | |

US-CL-CURRENT: 710/305

[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#) [Sequences](#) [Attachments](#) [Claims](#) [KIMC](#) [Drawn Desc](#) [Image](#)

5. Document ID: US 20010042180 A1

L3: Entry 5 of 15

File: PGPB

Nov 15, 2001

PGPUB-DOCUMENT-NUMBER: 20010042180
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20010042180 A1

TITLE: METHOD AND DEVICE TO USE MEMORY ACCESS REQUEST TAGS

6

PUBLICATION-DATE: November 15, 2001

INVENTOR-INFORMATION:

| | | | | |
|------------------|-----------|-------|---------|---------|
| NAME | CITY | STATE | COUNTRY | RULE-47 |
| LABERGE, PAUL A. | SHOREVIEW | MN | US | |

US-CL-CURRENT: 711/154; 711/105, 711/118[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KMC](#) | [Draw Desc](#) | [Image](#)**6. Document ID: US 6684303 B2**

L3: Entry 6 of 15

File: USPT

Jan 27, 2004

US-PAT-NO: 6684303

DOCUMENT-IDENTIFIER: US 6684303 B2

TITLE: Method and device to use memory access request tags

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KMC](#) | [Draw Desc](#) | [Image](#)**7. Document ID: US 6678803 B2**

L3: Entry 7 of 15

File: USPT

Jan 13, 2004

US-PAT-NO: 6678803

DOCUMENT-IDENTIFIER: US 6678803 B2

**** See image for Certificate of Correction ****

TITLE: Method and device to use memory access request tags

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KMC](#) | [Draw Desc](#) | [Image](#)**8. Document ID: US 6658508 B1**

L3: Entry 8 of 15

File: USPT

Dec 2, 2003

US-PAT-NO: 6658508

DOCUMENT-IDENTIFIER: US 6658508 B1

TITLE: Expansion module with external bus for personal digital assistant and design method therefor

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KMC](#) | [Draw Desc](#) | [Image](#)**9. Document ID: US 6571308 B1**

L3: Entry 9 of 15

File: USPT

May 27, 2003

US-PAT-NO: 6571308

DOCUMENT-IDENTIFIER: US 6571308 B1

TITLE: Bridging a host bus to an external bus using a host-bus-to-processor protocol translator

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KMC](#) | [Draw Desc](#) | [Image](#)

10. Document ID: US 6477609 B1

L3: Entry 10 of 15

File: USPT

Nov 5, 2002

US-PAT-NO: 6477609

DOCUMENT-IDENTIFIER: US 6477609 B1

TITLE: Bridge state-machine progression for data transfers requested by a host bus and responded to by an external bus

[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#) [Claims](#) [IAC](#) [Drawn Desc](#) [Image](#)

[Clear](#)

[Generate Collection](#)

[Print](#)

[Fwd Refs](#)

[Bkwd Refs](#)

[Generate OACS](#)

Terms

Documents

L2 same programmable

15

Display Format: [Change Format](#)

[Previous Page](#)

[Next Page](#)

[Go to Doc#](#)